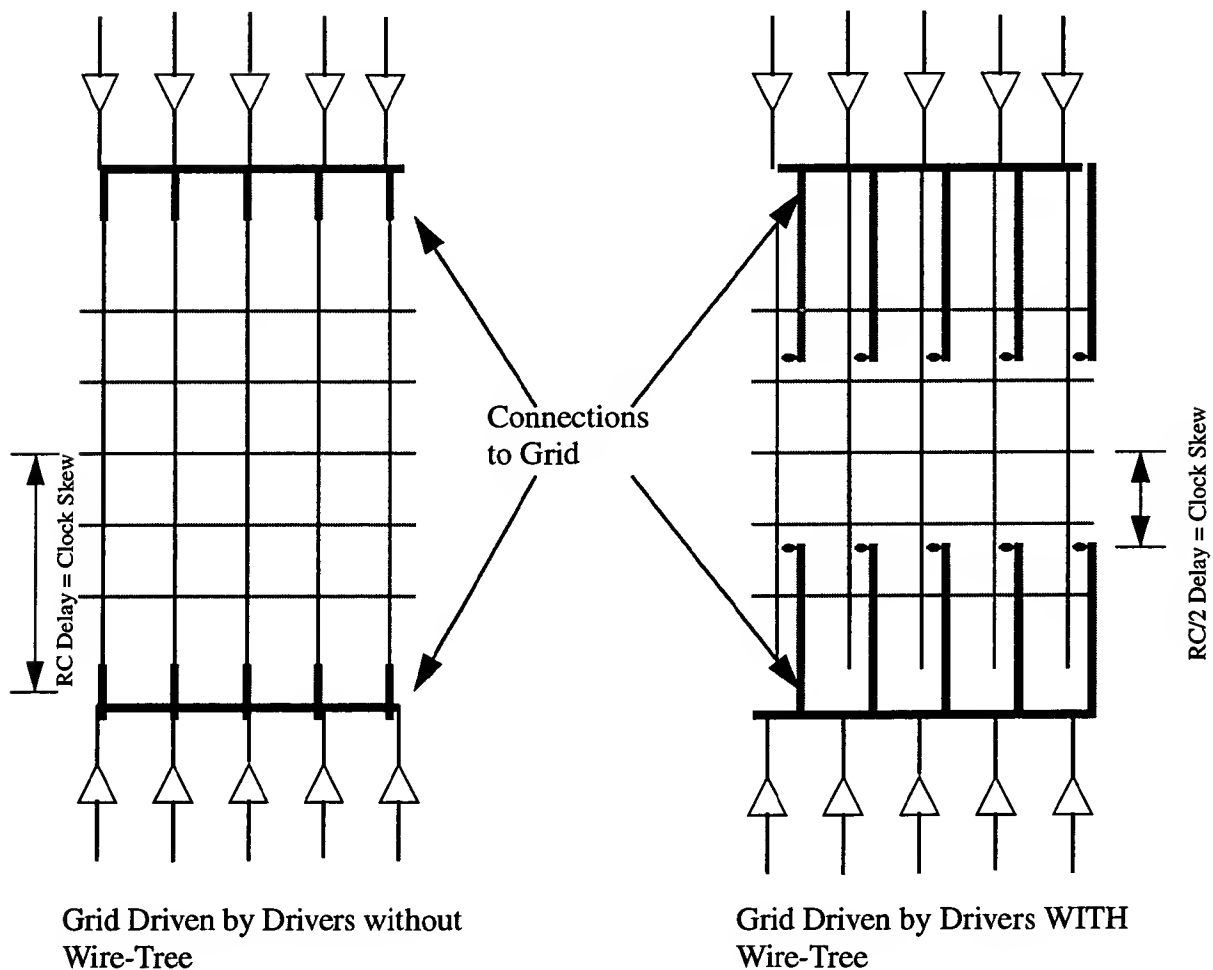
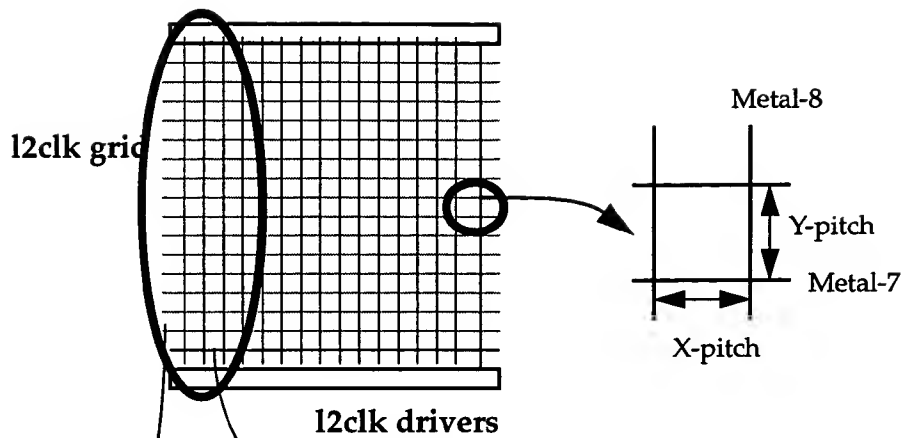
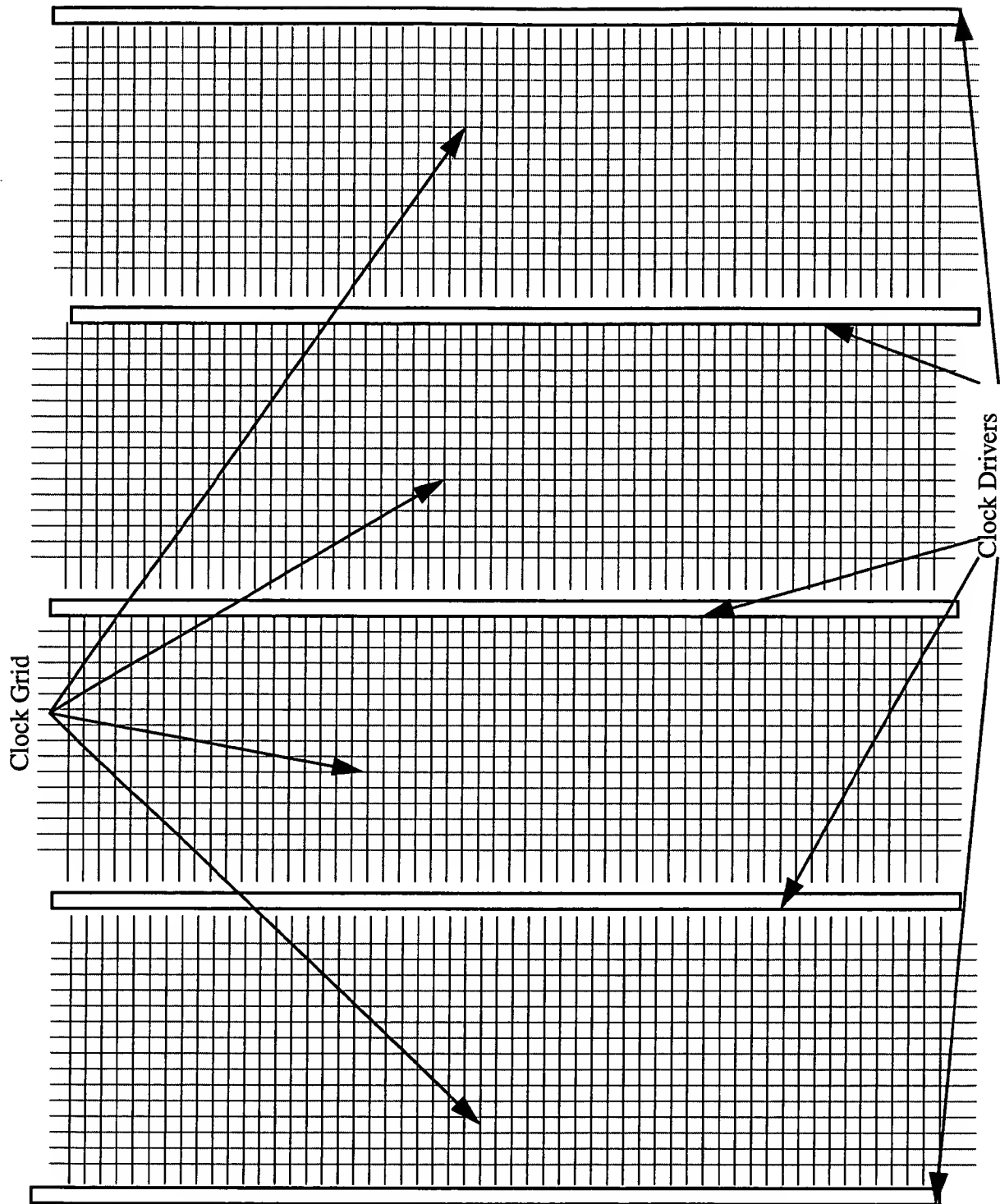
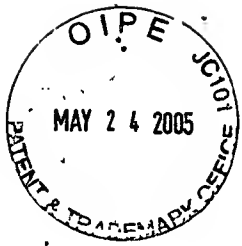


**Clock Distribution Scheme**





Typical Clock Grid and Clock Driver Arrangement for a